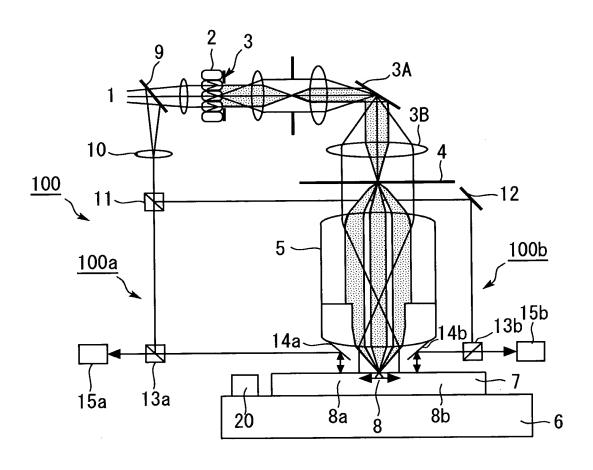
Inventor(s): Tetsuro HANAWA DOCKET NO.: 025311-0111

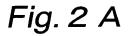
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DOCKET NO.: 025311-0111





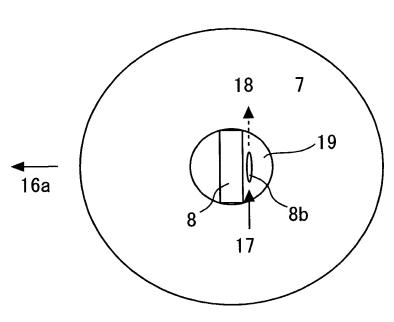
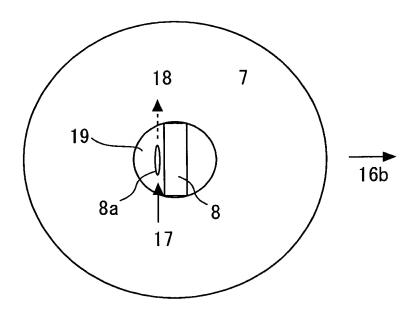


Fig. 2 B



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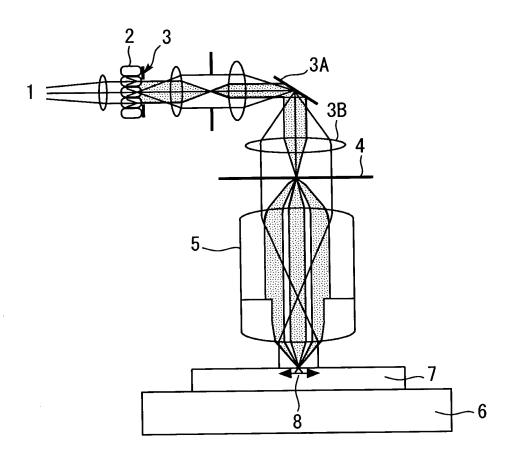
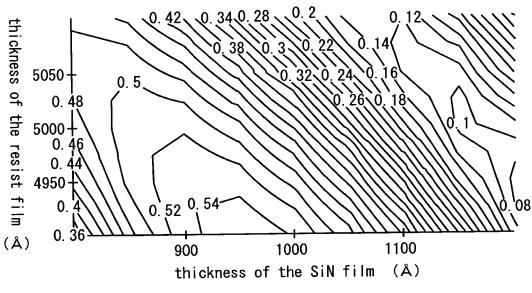




Fig. 4



7c: resist:5000±100 Å 7b: SiN:1000±100 Å 7a: Si substrate 7: wafer

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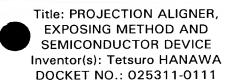
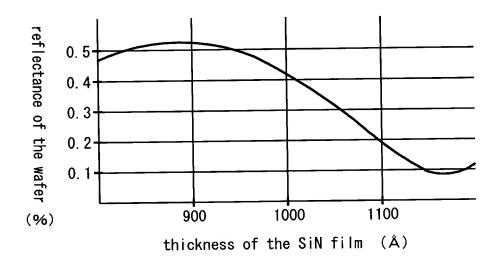
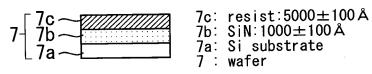


Fig. 5

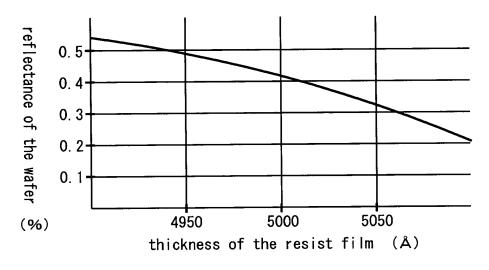


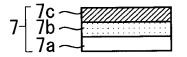


Inventor(s): Tetsuro HANAWA DOCKET NO.: 025311-0111

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Fig. 6





7c: resist:5000±100Å
7b: SiN:1000±100Å
7a: Si substrate
7: wafer

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Fig. 7

